MTS-3449US PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No:

To Be Assigned

Applicant:

Koji Takinami et al.

Filed:

1

July 15, 2003

Title:

OSCILLATOR, PLL CIRCUIT, COMMUNICATION EQUIPMENT, AND

OSCILLATING METHOD

TC/A.U.:

To Be Assigned

Confirmation: Examiner:

To Be Assigned To Be Assigned

Docket No.:

MTS-3449US

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (R&P) submitted herewith. A copy of each of the listed references is also enclosed.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

The following Japanese Reference is not in the English language:

1) 2000-224027

Reference 1) includes an English language abstract.

Further Reference 1) is not in the English language, but a "concise explanation" under 37CFR § 1.98(a)(3) is set forth in the background portion of the specification on page 1, line 15.

This Information Disclosure Statement is being filed simultaneously with the above-referenced application. No fee or certification is required. 37 C.F.R. § 1.97(b).

Respectfully submitted,

RatnerPresi

Allan Ratner, Reg. No. 19,717 Attorney for Applicants

AR/fp

Enclosures:

PTO Form 1449

Copies of (3) References

Dated:

July 15, 2003

P.O. Box 980

Valley Forge, PA 19482-0980

(610) 407-0700

The Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. **18-0350** of any fees associated with this communication.

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I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Commissioner for Patents, Alexandria, VA 22312.

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PTO/SB/08A (AW 4/25/03) (04-03) Approved for use through 04/30/2004, OMB 0651-0031

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MTS-3449US

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	Application Number	To Be Assigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Filing Date	July 15, 2003	
	First Named Inventor	Koji Takinami et al.	
	Art Unit ·	To Be Assigned	
	Examiner Name	To Be Assigned	
			-

Attorney Docket No.

U.S. PATENT DOCUMENTS				
6 11	Document Number			Pages, Columns, Lines, Where
No.1	Number - Kind Code ^{2 (if known)}	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
	US 6,292,065 B1	09/18/01	Friedman et al.	Entire document.
	Cite No.¹	Cite	Cite No.1 Number - Kind Code (if known) Publication Date (MM-DD-YYYY)	Cite No.1 Number - Kind Code (if known) Publication Date (MM-DD-YYYY) Name of Patentee or Applicant of Cited Document

SHEET 1 of 2

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date (MM-DD-YYYY)	Name of Patentee or	,	
		Country Code ³ - Number ⁴ - Kind Code ⁵ (If known)		Applicant of Cited Document		
		JP 2000-224027 A (with English abstract)	08/11/00	MATSUSHITA ELEC IND CO LTD	Entire document.	
	<u></u>					
	······································					
Examiner Signature				Date Considered		

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at <u>www.uspto.gov</u> or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

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⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language translation is attached.

The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:

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Substitute for Form 1449/PTO	Compl te if Kn wn		
	Application Number	To Be Assigned	
INFORMATION DISCLOSURE	Filing Date	July 15, 2003	
STATEMENT BY APPLICANT (Use as many sheets as necessary)	First Named Inventor	Koji Takinami et al.	
	Art Unit	To Be Assigned	
	Examiner Name	To Be Assigned	
SHEET 2 of 2	Attorney Docket No.	MTS-3449US	

NON-PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²	
		Li Lin et al., "A 1.4GHx Differential Low-Noise CMOS Frequency Synthesizer using a Wideband PLL Architecture", Digest of Technical Papers, 2000 IEEE International Solid-State Circuits Conference, pp. 204 and 205.		
Examiner Signature		Date Considered		

Examiner	Date	

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